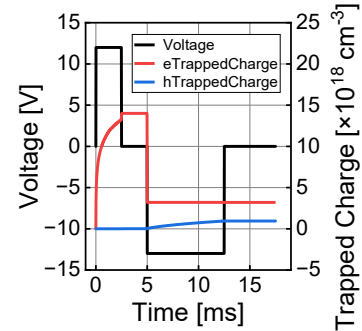
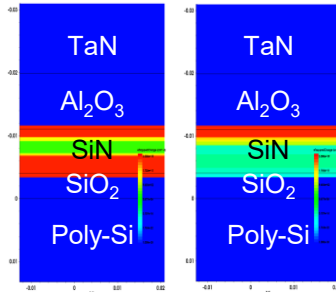
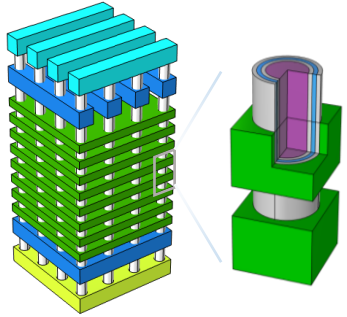
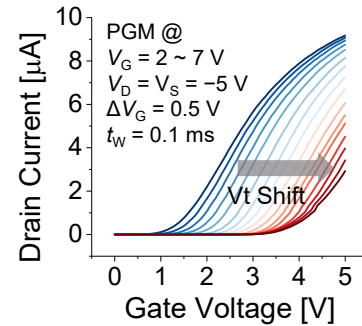
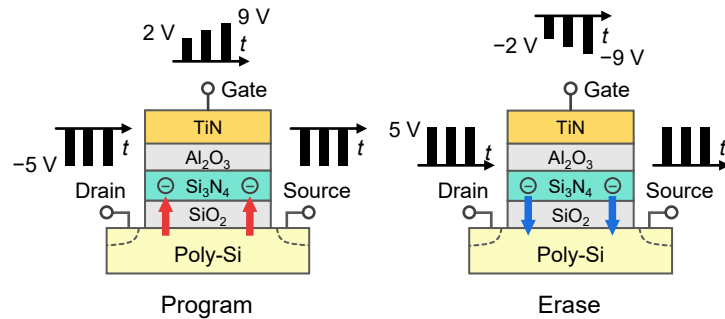


Flash Memory

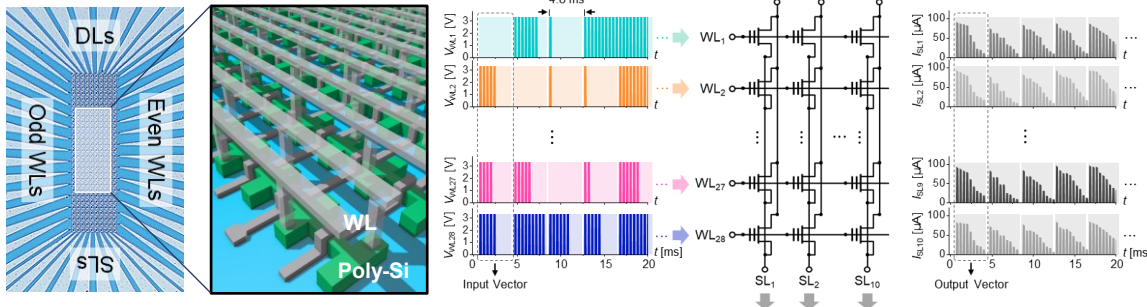
TCAD Simulations



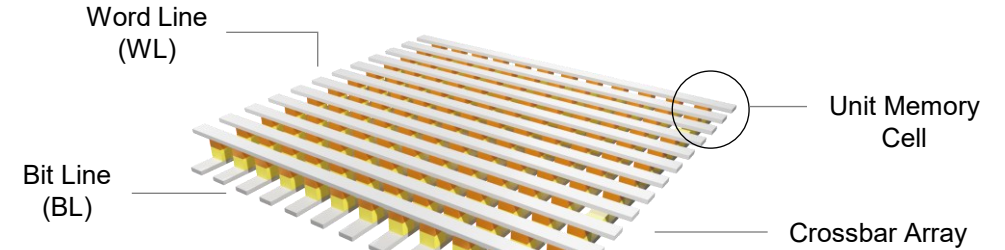
Device Characterization



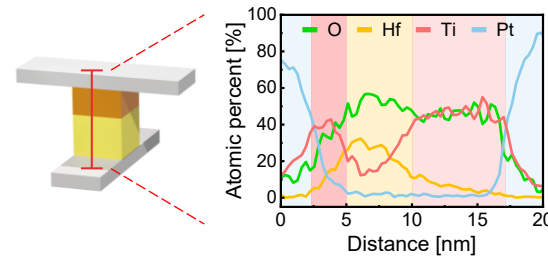
Array-Level Operating Scheme



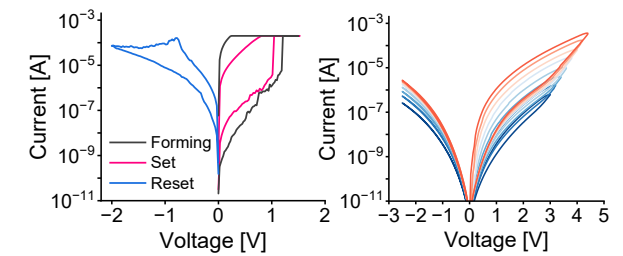
Emerging Memories



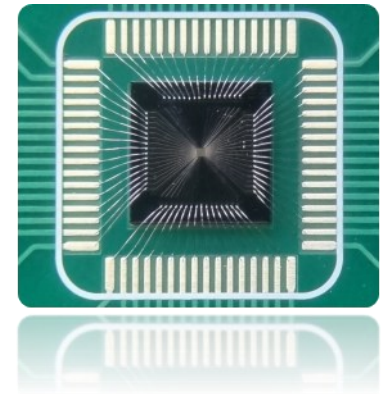
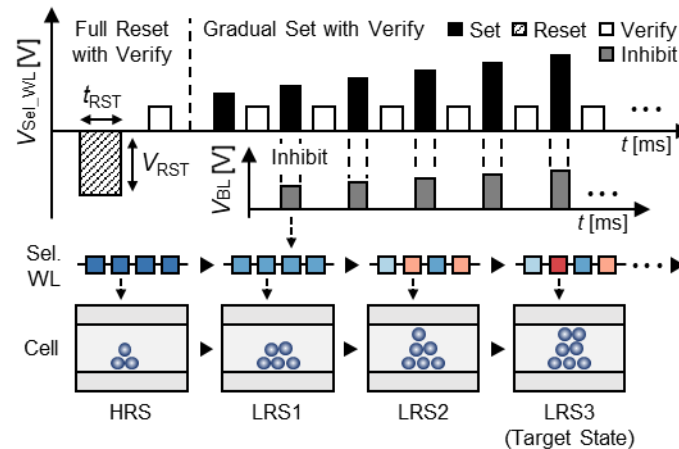
Material-Level Analysis



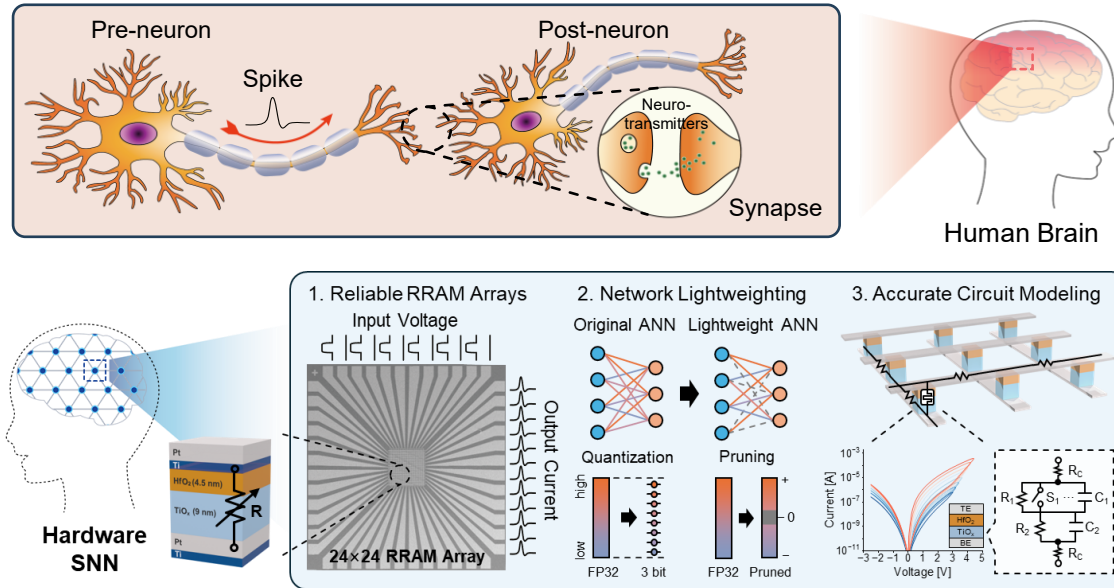
Device Characterization



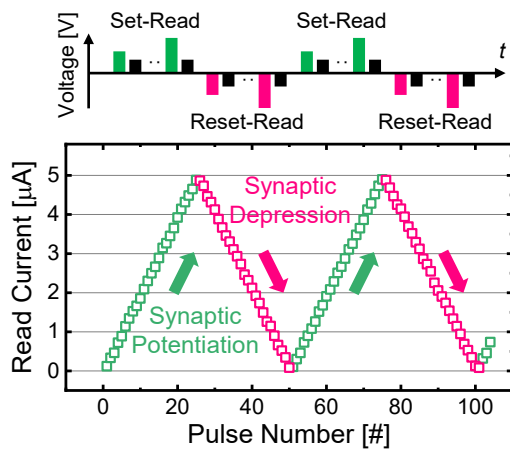
Array-Level Operating Scheme



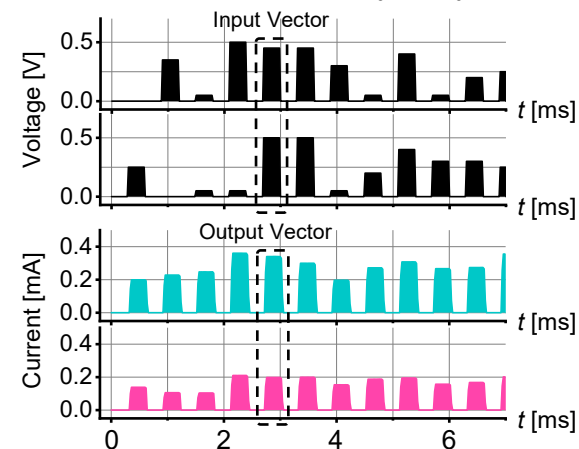
Synaptic Devices



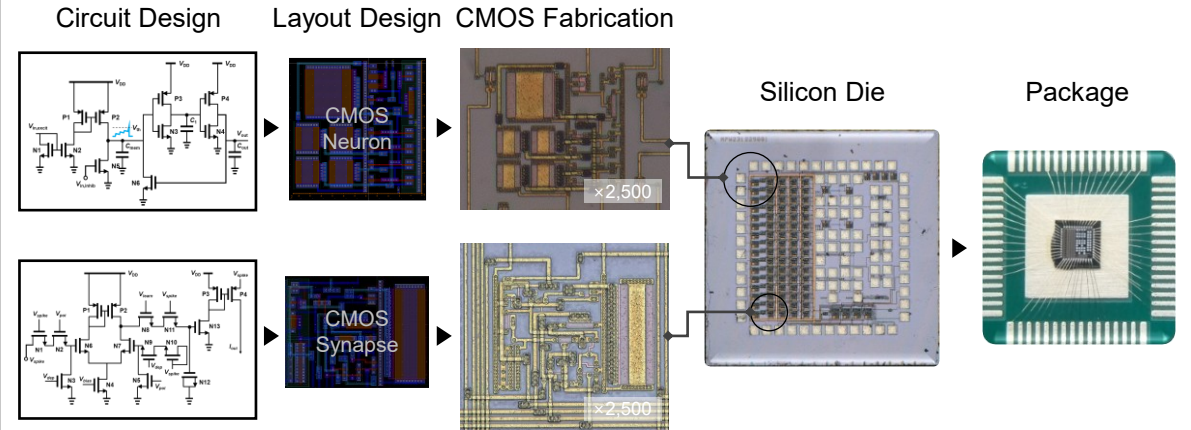
Synaptic Characteristics



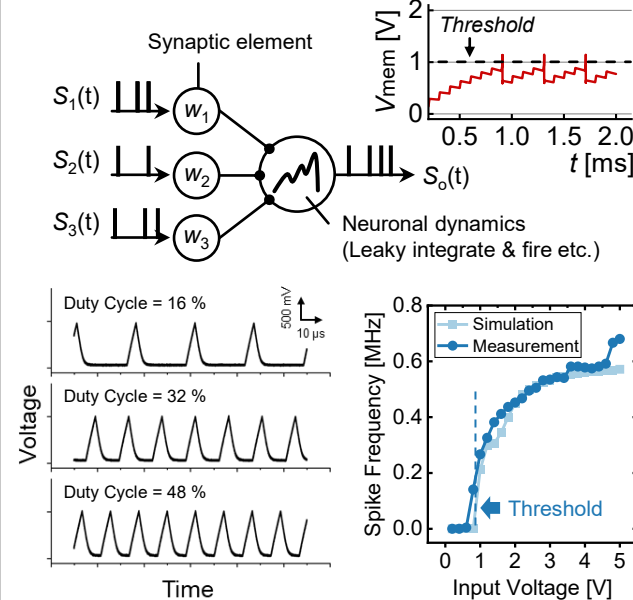
VMM Inference in Memory Array



Circuit Design for Artificial Neural System



Neuronal Behavior



Spiking Neural Network

